

## PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10004741-1

SERIAL NO.

APPLICANT

Osamu Samuel NAKAGAWA et al.

FILING DATE

Herewith

GROUP

N/A

09/01/04  
07/27/01

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
MC	1A	5,758,130	May 26, 1998	Michael J. DHUEY	395	552
MC	1B	6,075,832	Jun. 13, 2000	George GEANNOPOULOS et al.	375	375
MC	1C	5,849,610	Dec. 15, 1998	Qing ZHU	438	129
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

MC	1Q	LIU, X. et al., "Maximizing Performance by Retiming and Clock Skew Scheduling", Design Automation Conference, 1999, pages 231-236.
MC	1R	PAPAEFTHYMIU, M. et al., "Retiming and Clock Scheduling for High-Performance Synchronous Circuits", 10 pages, (date unknown).
MC	1S	KOURTEV, I. et al., "Timing Optimization Through Clock Skew Scheduling", Kluwer Academic Publishers, 2000, pages 1-194.

EXAMINER

DATE CONSIDERED

8/9/04